

| | L # | Hits | Search Text | DBs | Time Stamp |
|----|---------|-------|--|-----------|---------------------|
| 1 | L1 | 63407 | 327/\$.ccls. or 323/\$.ccls. | USPA T | 2001/04/26 18:57 |
| 2 | L2 | 4168 | 1 and current adj mirror | USPA T | 2001/04/26 18:58 |
| 3 | L3 | 334 | 2 and capacitor and transistor and latch | USPA T | 2001/04/26 18:58 |
| 4 | L4 | 209 | 3 and inverter | USPA T | 2001/04/26 19:11 |
| 5 | L5 | 599 | filter and capacitor and transistor and impedance same (pulse\$ near2 generat\$) | USPA T | 2001/04/26 19:16 |
| 6 | L6 | 595 | 5 not 3 | USPA T | 2001/04/26 19:16 |
| 7 | L7 | 24 | 6 and edge near2 detect\$ | USPA T | 2001/04/26 19:19 |
| 8 | L8 | 571 | 6 not 7 | USPA T | 2001/04/26 19:20 |
| 9 | L9 | 113 | 8 and latch | USPA T | 2001/04/26 19:20 |
| 10 | L1 0 | 113 | 9 not 3 | USPA T | 2001/04/26 19:25 |
| 11 | L1 1 | 351 | current adj mirror and capacitor near3 (receiv\$ or coupl\$) near2 input | USPA T | 2001/04/26 19:29 |
| 12 | L1 2 | 348 | 11 not 6 | USPA T | 2001/04/26 19:29 |

| | L # | Hits | Search Text | DBs | Time Stamp |
|---|-----|-------|--|-----------|---------------------|
| 1 | L1 | 50 | (("0287537") or ("5872721") or ("4035072") or ("5559664") or ("5218196") or ("5182810") or ("4952817") or ("5717725") or ("5710798") or ("5745532") or ("5923830") or ("6141770") or ("5260689") or ("5216613") or ("4656463") or ("5446759") or ("4827395") or ("5099153") or ("5140197") or ("5381540") or ("5189598") or ("5097154") or ("4388927") or ("5913826") or ("5553070") or ("5631854") or ("5661349") or ("5774702") or ("6072783") or ("5901246") or ("5875108") or ("6081750") or ("5401973") or ("5434420") or ("5451794") or ("5410551") or ("6120460") or ("6123668") or ("5903717") or ("5369771") or ("5587955") or ("5920096") or ("5210846") or ("5398326") or ("5627361") or ("6108751") or ("5748891") or ("6002708") or | USPA T | 2001/04/26 14:22 |
| 2 | L2 | 18818 | filter and latch | USPA T | 2001/04/26 14:23 |
| 3 | L3 | 1 | 2 and soft adj latch | USPA T | 2001/04/26 14:24 |
| 4 | L4 | 857 | 2 and edge adj detect\$ | USPA T | 2001/04/26 14:25 |

| | L # | Hits | Search Text | DBs | Time Stamp |
|----|---------|-------|---|-----------|---------------------|
| 5 | L5 | 732 | 4 and pulse | USPA T | 2001/04/26 14:26 |
| 6 | L6 | 188 | 5 and pulse adj signal | USPA T | 2001/04/26 14:26 |
| 7 | L7 | 54 | 6 and inverters | USPA T | 2001/04/26 14:27 |
| 8 | L8 | 153 | 6 and invert\$ | USPA T | 2001/04/26 14:48 |
| 9 | L9 | 5515 | current adj mirror and capacitor | USPA T | 2001/04/26 14:51 |
| 10 | L1 0 | 1783 | 9 and inverter and transistors | USPA T | 2001/04/26 14:51 |
| 11 | L1 1 | 184 | 10 and driv\$ adj transistor | USPA T | 2001/04/26 15:24 |
| 12 | L1 2 | 63407 | 327/\$.ccls. or 323/\$.ccls. | USPA T | 2001/04/26 15:24 |
| 13 | L1 3 | 4168 | 12 and current adj mirror | USPA T | 2001/04/26 15:25 |
| 14 | L1 4 | 2140 | 13 and capacitor and transistor | USPA T | 2001/04/26 15:25 |
| 15 | L1 5 | 746 | 14 and inverter | USPA T | 2001/04/26 15:26 |
| 16 | L1 6 | 743 | 15 not 8 | USPA T | 2001/04/26 15:26 |
| 17 | L1 7 | 0 | (control\$ adj impedance) near2 capacitor and 16 | USPA T | 2001/04/26 15:28 |
| 18 | L1 8 | 7 | (control\$ adj impedance) and 16 | USPA T | 2001/04/26 15:29 |
| 19 | L1 9 | 736 | 16 not 18 | USPA T | 2001/04/26 15:30 |